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to compute. Recently, an effective **observability-based** statement coverage metric was proposed
Simulation Vector Generation from **HDL** Descriptions for Observability-Enhanced Statement
hordes of "verification engineers" poring over the **HDL** code) and actual simulation time both contribute
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of the design. Motivated by recent work on **observability-based** coverage metrics for models described in a
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Metric (OCCOM) 6] can be done while simulating an **HDL** design. This computation is done with the help of
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we adopted into our framework the **Observability-Based** Code Coverage (OBCC or tag coverage)7]
Automatic Design Validation Framework For **Hdl** Descriptions Via Rtl Atpg Liang Zhang And
statements, and arithmetic expressions in the **HDL** description. A test environment is a set of
www.visc.vt.edu/~mhsiao/papers/ats03lz.pdf

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effect of the fault has to be propagated to an **observable** circuit output in order for the fault to be
description in some Hardware Description language (**HDL**) or structural RTL description. Statements in the
In order to compute observability information we **tag** variables during simulation and use a simulation
glenfiddich.lcs.mit.edu/~devadas/pubs/coverage.ps

Simulation Vector Generation from HDL Descriptions for.. - Fallah, Ashar, Devadas (1999) (Correct) (20 citations)
propagate the effect of the error to an **observable** 1 output. A coverage metric must begin with
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to a fault-list in test generation, we maintain a "**tag**-list" during vector generation. **Tags** are
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effect of the fault has to be propagated to an **observable** circuit output in order for the fault to be
(OCCOM) that can be used while simulating complex **HDL** designs. This method offers a more accurate
create better functional tests. The concurrent **tag** propagation algorithm proposed in this paper
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that are activated by the test vectors are **observable** at the outputs. Observability information is
Verona, ITALY Abstract Functional testing of **HDL** specifications is one of the most promising
coverage, path coverage and the most recent **tag** coverage are not sufficient to guarantee the
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metric computes the number of **tags** that reach an **observable** circuit output when the test pattern is
higher, thanks to the availability and maturity of **HDL** simulators and synthesis tools. On the other
Statement Coverage. They define the concept of **tag** as the possibility that an incorrect value is
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